

1reg = 2bytes (16bit)
 short int = 16 bit
 integer = 32 bit
 long int = 64 bit

Instantaneous Parameter								
Reg Addr (Hex)	Register	(R/W)	Unit	Multiplier	Format	Comment	Reg Byte Count	Reg Addr (Dec)
0000	Reserved				-		2	0
0001	Ampere L1	R	%	0.1	short int		2	1
0002	Ampere L2	R	%	0.1	short int		2	2
0003	Ampere L3	R	%	0.1	short int		2	3
0004	Reserved						2	4
0005							2	5
0006	Relay Flag , Trip Flag	R	-	-	short int	current trp flg, rlyflg	2	6
0007	Current trip mem L1	R	%	0.1	short int		2	7
0008	Current trip mem L2	R	%	0.1	short int		2	8
0009	Current trip mem L3	R	%	0.1	short int		2	9
000A	Reserved						2	10
000B	last trp elapse	R	sec	1	integer		4	11
000D	Reserved						4	13
000F	operation hour	R	min	1	integer		4	15

Trip Event								
Reg Addr (Hex)	Register	(R/W)	Unit	Multiplier	Format	Comment	Reg Byte Count	Reg Addr (Dec)
0011	trp event status mem b	R	-	-	short int	trpflg for mem b	2	17
0012	trp event mem b-L1	R	%	0.1	short int	trip value L1 for mem b	2	18
0013	trp event mem b-L2	R	%	0.1	short int	trip value L2 for mem b	2	19
0014	trp event mem b-L3	R	%	0.1	short int	trip value L3 for mem b	2	20
0015	Reserved						2	21
0016	trp event status mem c1	R	-	-	short int	trpflg for mem c1	2	22
0017	trp event mem c1-L1	R	%	0.1	short int	trip value L1 for mem c1	2	23
0018	trp event mem c1-L2	R	%	0.1	short int	trip value L2 for mem c1	2	24
0019	trp event mem c1-L3	R	%	0.1	short int	trip value L3 for mem c1	2	25
001A	Reserved						2	26
001B	trp event status mem c2	R	-	-	short int	trpflg for mem c2	2	27
001C	trp event mem c2-L1	R	%	0.1	short int	trip value L1 for mem c2	2	28
001D	trp event mem c2-L2	R	%	0.1	short int	trip value L2 for mem c2	2	29
001E	trp event mem c2-L3	R	%	0.1	short int	trip value L3 for mem c2	2	30
001F	Reserved						2	31
0020	trp event status mem c3	R	-	-	short int	trpflg for mem c3	2	32
0021	trp event mem c3-L1	R	%	0.1	short int	trip value L1 for mem c3	2	33
0022	trp event mem c3-L2	R	%	0.1	short int	trip value L2 for mem c3	2	34
0023	trp event mem c3-L3	R	%	0.1	short int	trip value L3 for mem c3	2	35
0024	Reserved						2	36
0025	trp event status mem c4	R	-	-	short int	trpflg for mem c4	2	37
0026	trp event mem c4-L1	R	%	0.1	short int	trip value L1 for mem c4	2	38
0027	trp event mem c4-L2	R	%	0.1	short int	trip value L2 for mem c4	2	39
0028	trp event mem c4-L3	R	%	0.1	short int	trip value L3 for mem c4	2	40
0029	Reserved						2	41
002A	trp event status mem c5	R	-	-	short int	trpflg for mem c5	2	42
002B	trp event mem c5-L1	R	%	0.1	short int	trip value L1 for mem c5	2	43
002C	trp event mem c5-L2	R	%	0.1	short int	trip value L2 for mem c5	2	44
002D	trp event mem c5-L3	R	%	0.1	short int	trip value L3 for mem c5	2	45
002E	Reserved						2	46
002F	trp event status mem c6	R	-	-	short int	trpflg for mem c6	2	47
0030	trp event mem c6-L1	R	%	0.1	short int	trip value L1 for mem c6	2	48
0031	trp event mem c6-L2	R	%	0.1	short int	trip value L2 for mem c6	2	49
0032	trp event mem c6-L3	R	%	0.1	short int	trip value L3 for mem c6	2	50
0033	Reserved						2	51

Fault Event								
Reg Addr (Hex)	Register	(R/W)	Unit	Multiplier	Format	Comment	Reg Byte Count	Reg Addr (Dec)
0034	fault event mem Phase	R	-	-	short int	Hi byte = 0, Lo byte = phase mem	2	52
0035	fault event mem E1	R	%	0.1	short int		2	53
0036	fault event mem E2	R	%	0.1	short int		2	54
0037	fault event mem E3	R	%	0.1	short int		2	55
0038	fault event mem E4	R	%	0.1	short int		2	56

Setting Parameter								
Reg Addr (Hex)	Register	(R/W)	Unit	Multiplier	Format	Comment	Reg Byte Count	Reg Addr (Dec)
0F00	model	R	-	-	short int	0x0023 (DP-23), 0x0033 (DP-33)	2	3840
0F01	firmware Ver, Map Ver	R	-	0.01	short int	203, 100	2	3841
0F02	lp >	R	%	1	short int	2 ~ 200	2	3842
0F03	char lp > , TMs (IDMT mode only)	R	-	1	short int	Hi byte = tripping characteristic, Lo byte = TMs	2	3843
0F04	tp > (DTL mode only)	R	s	0.01	short int	3 ~ 2000	2	3844
0F05	lp >>	R	%	1	short int	OFF = 0 or 20 ~ 2000	2	3845
0F06	tp >>	R	s	0.01	short int	Instant = 0 or 2 ~ 50	2	3846
0F07	General	R	-	-	short int	Relay Function Setting	2	3847

